

**Notice of References Cited**

Application/Control No.

10/696,146

Applicant(s)/Patent Under  
Reexamination  
GALLES ET AL.

Examiner

William M. Treat

Art Unit

2181

Page 1 of 1

**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-			
	B	US-			
	C	US-			
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

**FOREIGN PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Kenneth Yeager, "The MIPS R10000 Superscalar Microprocessor", IEEE Micro, vol. 16, iss. 2, April 1996, pp. 28-41.
	V	Agarwal et al., "An Evaluation of Directory Schemes for Cache Coherence", Proceedings of the 15 <sup>th</sup> International Symposium on Computer Architecture, IEEE, May 30 - June 2, 1988, pp. 280-289.
	W	
	X	

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.